UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)

Joseph J. Nahas

GROUP ART UNIT: 2123

APPLN. NO.:

10/736,852

EXAMINER: Guy J. Lamarre

FILED:

December 16, 2003

CONFIRMATION No.: 6904

TITLE:

MRAM HAVING ERROR CORRECTION CODE CIRCUITRY AND

METHOD THEREFOR

Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA by submission via the EFS web system.

Date of Submission

Signature

Pat Thomas

Printed Name of Person Signing Certificate

RESPONSE UNDER 37 C.F.R. 1.111

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This paper is responsive to an Office Action mailed September 11, 2007, having a shortened statutory period of three months expiring December 11, 2006. Further examination and reconsideration are respectfully requested in view of the election made below.

<u>FEES</u>

No additional fees are believed owed as a result of this communication. However, Applicant continues to authorize the payment of any fees owed in connection with this application from Deposit Account No. 503079 (Freescale Semiconductor, Inc.), or credit Deposit Account No. 503079 for any refunds.